

IN THE CLAIMS

Please cancel claims 1-13 and 17-21.

Please add new claims 22-33 as set forth below:

22. (New) A method of burning-in a static random access memory (SRAM) comprising:
- selecting a plurality of memory array blocks in the SRAM; and
 - simultaneously writing to each of the plurality of memory array blocks during burn-in testing.
23. (New) The method, as set forth in claim 22, wherein selecting comprises providing a block selection signal and a testmode signal to a logical device.
24. (New) The method, as set forth in claim 22, wherein selecting comprises providing a clock selection signal and a testmode signal to a NAND gate.
25. (New) The method, as set forth in claim 22, wherein simultaneously writing comprises transmitting data to a plurality of local write drivers.
26. (New) The method, as set forth in claim 25, comprising driving the data from each of the plurality of local write drivers to a respective one of the plurality of memory array blocks.

27. (New) A method of burning-in a static random access memory (SRAM) comprising:
- combining a plurality of signals produced externally with respect to the SRAM;
 - producing an internal signal configured to simultaneously select a plurality of memory array blocks in the SRAM; and
 - simultaneously writing to each of the plurality of memory array blocks.
28. (New) The method, as set forth in claim 27, wherein combining comprises combining the plurality of signals through a logical device located internally with respect to the SRAM.
29. (New) The method, as set forth in claim 28, wherein producing comprises producing an internal signal from an output of the logical device.
30. (New) The method, as set forth in claim 27, wherein combining comprises combining the plurality of signals through a logical device located internally with respect to the NAND gate.
31. (New) The method, as set forth in claim 27, wherein simultaneously writing comprises simultaneously writing to each of the plurality of memory array blocks during burn-in.

32. (New) The method, as set forth in claim 27, wherein simultaneously writing comprises transmitting data to a plurality of local write drivers.

33. (New) The method, as set forth in claim 32, comprising driving the data from each of the plurality of local write drivers to a respective one of the plurality of memory array blocks.

Conclusion

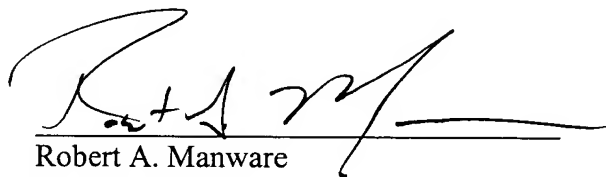
In view of the remarks set forth above, Applicants respectfully request consideration of present claims 14-16 and 22-33. If the Examiner believes that a telephonic interview will help speed this application toward issuance, the Examiner is invited to contact the undersigned at the telephone number listed below.

General Authorization for Payment of Fees and Extensions of Time

In accordance with 37 C.F.R. § 1.136, Applicants hereby provide a general authorization to treat this and any future reply requiring an extension of time as incorporating a request therefor. Furthermore, Applicants authorize the Commissioner to charge the appropriate fee for the additional claims and for any extension of time to Deposit Account No. 13-3092; Order No. MICS:0056--1/FLE (00-0301.01).

Respectfully submitted,

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